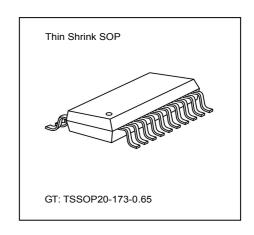


# All-Ways-On<sup>™</sup> Constant-Current LED Driver

#### **Features**

- 16 constant-current output channels
- Constant output current invariant to load voltage change
- Excellent output current accuracy: between channels: <±3% (max.), and between ICs: <±6% (max.)
- Output current adjusted through an external resistor
- Constant output current range per channel: 5-60 mA
- Schmitt trigger input
- 5V supply voltage
- Package Type: "Pb-free & Green" package with thermal pad



| Current A        | Conditions  |                              |
|------------------|-------------|------------------------------|
| Between Channels | Between ICs | Conditions                   |
| < ±3%            | < ±6%       | I <sub>OUT</sub> = 5 ~ 60 mA |

#### **Product Description**

MBI1816 is an instant On/Off LED driver for lighting applications and exploits PrecisionDrive™ technology to enhance its output characteristics. At MBI1816 output stage, sixteen regulated current ports are designed to provide uniform and constant current sinks for driving LEDs within a large range of V<sub>F</sub> variations.

MBI1816 provides users 16-channel constant current ports to match LEDs with equal current. Users may adjust the output current from 5 mA to 60 mA through an external resistor, Rext, which gives users flexibility in controlling the light intensity of LEDs. In addition, users can precisely adjust LED brightness from 0% to 100% via output enable (OE) with Pulse Width Modulation.

Additionally, to ensure the system reliability, MBI1816 is built with Thermal Protection (TP) function and thermal pad. The TP function protects IC from over temperature (165°C). Also, the thermal pad enhances the power dissipation. As a result, a large amount of current can be handled safely in one package.

### **Applications**

- Automotive interior lighting
- Channel letter
- **Decoration lighting**

# **Typical Application Circuit**

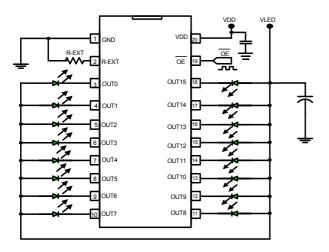
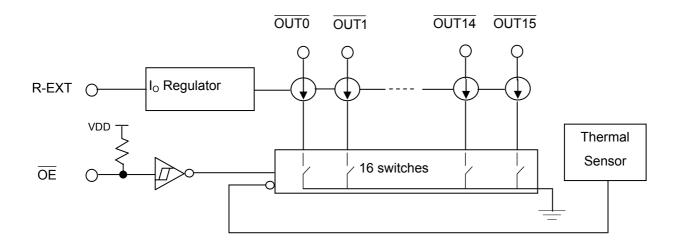


Figure 1

# **Functional Diagram**



# **Pin Configuration**

| GND   | 1  | 20 | VDD   |
|-------|----|----|-------|
| R-EXT | 2  | 19 | ŌĒ    |
| OUT0  | 3  | 18 | OUT15 |
| OUT1  | 4  | 17 | OUT14 |
| OUT2  | 5  | 16 | OUT13 |
| OUT3  | 6  | 15 | OUT12 |
| OUT4  | 7  | 14 | OUT11 |
| OUT5  | 8  | 13 | OUT10 |
| OUT6  | 9  | 12 | OUT9  |
| OUT7  | 10 | 11 | OUT8  |
|       |    |    |       |

**MBI1816GT** 

### **Pin Description**

| Pin Name    | Function                                                                                                                                           |
|-------------|----------------------------------------------------------------------------------------------------------------------------------------------------|
| GND         | Ground terminal for control logic and current sink                                                                                                 |
| OUT0~OUT15  | Constant current output terminals                                                                                                                  |
| ŌĒ          | Output enable terminal When OE is active (low), the output pins are enabled; when OE is inactive (high), all output pins are turned OFF (blanked). |
| R-EXT       | Terminal used to connect an external resistor (R <sub>ext</sub> ) for setting up output current for all output channels                            |
| VDD         | 5V supply voltage terminal                                                                                                                         |
| Thermal Pad | Power dissipation terminals connected to GND*                                                                                                      |

<sup>\*</sup>To eliminate the noise influence, the thermal pad is suggested to be connected to GND on PCB.

In addition, desired thermal conductivity will be improved, if a heat-conducting copper foil on PCB is soldered with thermal pad.

# **Maximum Ratings**

| Characteristic                                     |         | Symbol           | Rating                     | Unit |
|----------------------------------------------------|---------|------------------|----------------------------|------|
| Supply Voltage                                     |         | $V_{DD}$         | 0~7.0                      | V    |
| Input Voltage                                      |         | V <sub>IN</sub>  | -0.4~V <sub>DD</sub> + 0.4 | V    |
| Output Current                                     |         | I <sub>OUT</sub> | 90*                        | mA   |
| Sustaining Voltage                                 |         | V <sub>DS</sub>  | -0.5~+17.0                 | V    |
| GND Terminal Current                               |         | I <sub>GND</sub> | 1000                       | mA   |
| Power Dissipation*<br>(On PCB, Ta=25°C)            |         | P <sub>D</sub>   | 0.85                       | W    |
| Thermal Resistance** (By simulation)               | TSSOP20 | D                | 31.99                      | °C/W |
| Empirical Thermal Resistance*<br>(On PCB, Ta=25°C) |         | $R_{th(j-a)}$    | 117                        | C/VV |
| Operating Junction Temperature                     |         | $T_{j,max}$      | 125                        | °C   |
| Operating Temperature                              |         | T <sub>opr</sub> | -40~+85                    | °C   |
| Storage Temperature                                |         | T <sub>stg</sub> | -55~+150                   | °C   |

<sup>\*</sup>Users must notice that the power dissipation (almost equaling to  $I_{OUT} \times V_{DS}$ ) should be within the Safe Operation Area shown in Figure 7.

<sup>\*\*</sup>Provided by factory.

#### **Electrical Characteristics**

| Charac                       | teristic   | Symbol                              | Condition                                                                                                                                                                                                                                                                                             |                          | Min.                | Тур. | Max.                | Unit  |   |    |
|------------------------------|------------|-------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------|---------------------|------|---------------------|-------|---|----|
| Supply Voltage               | je         | $V_{DD}$                            | -                                                                                                                                                                                                                                                                                                     | -                        | 4.5                 | 5.0  | 5.5                 | V     |   |    |
| Sustaining Vo                | ltage at   | $V_{DS}$                            | OUT0 ~ OUT15                                                                                                                                                                                                                                                                                          |                          | -                   | -    | 17.0                | V     |   |    |
| Output Curren                | t          | I <sub>OUT</sub>                    | DC Test Circuit                                                                                                                                                                                                                                                                                       |                          | 5                   | -    | 60*                 | mA    |   |    |
| Innut \/altaga               | "H" level  | V <sub>IH</sub>                     | T <sub>a</sub> = -40~85°C                                                                                                                                                                                                                                                                             |                          | 0.7×V <sub>DD</sub> | -    | $V_{DD}$            | V     |   |    |
| Input Voltage                | "L" level  | V <sub>IL</sub>                     | T <sub>a</sub> = -40~85°C                                                                                                                                                                                                                                                                             |                          | GND                 | -    | 0.3×V <sub>DD</sub> | V     |   |    |
| Output Leakag                | ge Current | I <sub>OH</sub>                     | V <sub>OH</sub> = 17.0V                                                                                                                                                                                                                                                                               |                          | -                   | -    | 0.5                 | μA    |   |    |
| Output Curren                | t 1        | I <sub>OUT1</sub>                   | V <sub>DS</sub> = 0.6V                                                                                                                                                                                                                                                                                | R <sub>ext</sub> = 2.4kΩ | -                   | 30.5 | -                   | mA    |   |    |
| Current Skew                 | 1          | dl <sub>out</sub> /l <sub>out</sub> | I <sub>OL</sub> = 30.5mA<br>V <sub>DS</sub> = 0.6V                                                                                                                                                                                                                                                    | R <sub>ext</sub> = 2.4kΩ | -                   | ±1   | ±3                  | %     |   |    |
| Output Curren                | t 2        | I <sub>OUT2</sub>                   | V <sub>DS</sub> = 0.8V                                                                                                                                                                                                                                                                                | R <sub>ext</sub> = 1.3kΩ | -                   | 56   | -                   | mA    |   |    |
| Current Skew                 | 2          | dl <sub>out</sub> /l <sub>out</sub> | I <sub>OL</sub> = 56mA<br>V <sub>DS</sub> = 0.8V                                                                                                                                                                                                                                                      | R <sub>ext</sub> = 1.3kΩ | -                   | ±1   | ±3                  | %     |   |    |
| Regulation of Current vs. Su |            | %/dV <sub>DS</sub>                  | V <sub>DS</sub> within 1.0V and 3.0V                                                                                                                                                                                                                                                                  |                          | -                   | ±0.1 | -                   | % / V |   |    |
| Regulation of Current vs. Su | •          | %/dV <sub>DD</sub>                  | V <sub>DD</sub> within 4.5V and 5.5V                                                                                                                                                                                                                                                                  |                          | -                   | ±1   | -                   | % / V |   |    |
| Pull-up Resisto              | or         | R <sub>IN</sub> (up)                | ŌĒ                                                                                                                                                                                                                                                                                                    |                          | 250                 | 500  | 800                 | ΚΩ    |   |    |
| Junction Temp<br>Threshold   | erature    | T <sub>x</sub>                      | When $T_j$ approaches $T_x$ and $\overline{OUT}$ is shut off.                                                                                                                                                                                                                                         |                          | l                   |      | -                   | 165   | - | °C |
|                              |            | I <sub>DD</sub> (off) 1             | $R_{ext}$ = Open, $\overline{OUT0} \sim \overline{OUT15}$ = Off<br>$R_{ext}$ = 2.4k $\Omega$ , $\overline{OUT0} \sim \overline{OUT15}$ = Off<br>$R_{ext}$ = 1.3k $\Omega$ , $\overline{OUT0} \sim \overline{OUT15}$ = Off<br>$R_{ext}$ = 2.4k $\Omega$ , $\overline{OUT0} \sim \overline{OUT15}$ = On |                          | -                   | 5    | 9                   |       |   |    |
|                              | "OFF"      | I <sub>DD</sub> (off) 2             |                                                                                                                                                                                                                                                                                                       |                          | -                   | 6    | 10                  |       |   |    |
| Supply<br>Current            |            | I <sub>DD</sub> (off) 3             |                                                                                                                                                                                                                                                                                                       |                          | -                   | 8    | 12                  | mA    |   |    |
|                              | "ON"       | I <sub>DD</sub> (on) 1              |                                                                                                                                                                                                                                                                                                       |                          | -                   | 6    | 10                  |       |   |    |
|                              | ON         | I <sub>DD</sub> (on) 2              | $R_{\text{ext}} = 1.3 \text{k}\Omega, \ \overline{\text{OUT0}} \sim \overline{\text{OUT15}} = \text{On}$                                                                                                                                                                                              |                          | -                   | 7    | 10                  |       |   |    |

<sup>\*</sup> Each output current, I<sub>OUT</sub>, can be driven up to 90mA, but the total output current should be smaller than 1A.

### **Test Circuit for Electrical Characteristics**

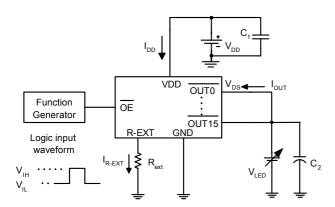


Figure 2

# **Switching Characteristics**

| Characteri                          |              | Symbol             | Condition                                                   | Min. | Тур. | Max. | Unit |
|-------------------------------------|--------------|--------------------|-------------------------------------------------------------|------|------|------|------|
| Propagation Delay Time ("L" to "H") | OE - OUTn    | t <sub>pLH</sub>   | V <sub>DD</sub> = 5.0 V<br>V <sub>DS</sub> = 1.0V           | 0.1  | 0.3  | 0.5  | μs   |
| Propagation Delay Time ("H" to "L") | OE - OUTn    | t <sub>pHL</sub>   | $V_{IH} = V_{DD}$<br>$V_{IL} = GND$                         | 0.05 | 0.1  | 0.2  | μs   |
| Pulse Width                         | ŌE           | t <sub>w(OE)</sub> | $R_{\text{ext}}$ = 1229 $\Omega$ ( $I_{\text{OUTn}}$ =60mA) | 1    | -    | -    | μs   |
| Output Rise Time of OU              | T (turn off) | t <sub>or</sub>    | $V_L = 4.0 \text{ V}$<br>$R_1 = 44.62 \Omega$               | 0.1  | 0.3  | 0.5  | ns   |
| Output Fall Time of OUT             | (turn on)    | t <sub>of</sub>    | C <sub>L</sub> = 10 pF                                      | 0.1  | 0.3  | 0.5  | ns   |

# **Test Circuit for Switching Characteristics**

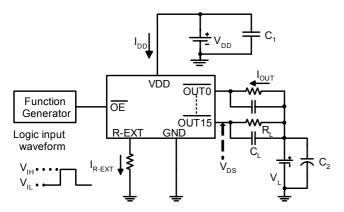
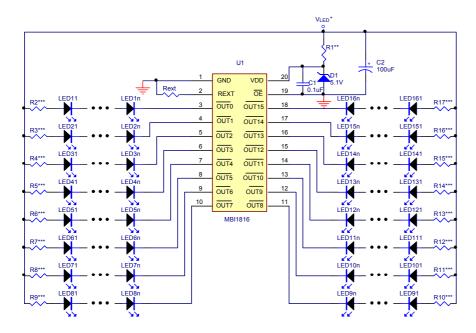


Figure 3

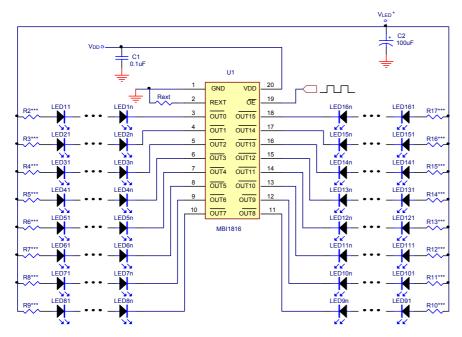
#### **Application Circuits**

(a) MBI1816 application circuit, where  $V_{\text{LED}}$  and  $V_{\text{DD}}$  share a single voltage source.



<sup>\*</sup>  $V_{LED}$  >  $V_{DS}$  +  $V_{F,LED}$  x n;  $V_{F,LED}$ : Forward voltage of LED; n: LED count \*\* R1 = ( $V_{LED}$  - 5.1V) /  $I_{DD}$ ; refer to Electrical Characteristics for  $I_{DD}$ 

(b) MBI1816 application circuit with dimming control by PWM signal, where  $V_{\text{LED}}$  and  $V_{\text{DD}}$  use voltage sources separately.



\* $V_{LED} = V_{DS} + V_{F,LED} x n; V_{F,LED}$ : Forward voltage of LED; n: LED count

Figure 4

<sup>\*\*\*</sup> R2~R17 =  $[V_{LED} - V_{DS} - (V_{F,LED} \times n)] / I_{LED}$ 

#### **Constant Current**

In LED lighting applications, MBI1816 provides nearly no variation in current from channel to channel and from IC to IC. This can be achieved by:

- 1) The maximum current variation between channels is less than  $\pm 3\%$ , and that between ICs is less than  $\pm 6\%$ .
- 2) In addition, the current characteristic of output stage is flat and users can refer to the figure as shown below. The output current can be kept constant regardless of the variations of LED forward voltages (V<sub>F</sub>). This guarantees LED to be performed on the same brightness as user's specification.

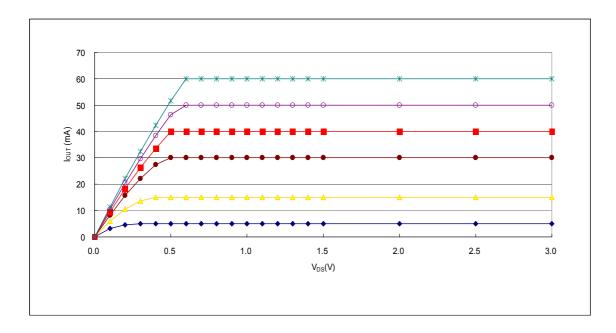


Figure 5

### **Setting Output Current**

The output current of each channel ( $I_{OUT}$ ) is set by an external resistor,  $R_{ext}$ . The relationship between  $I_{out}$  and  $R_{ext}$  is shown in the following figure.

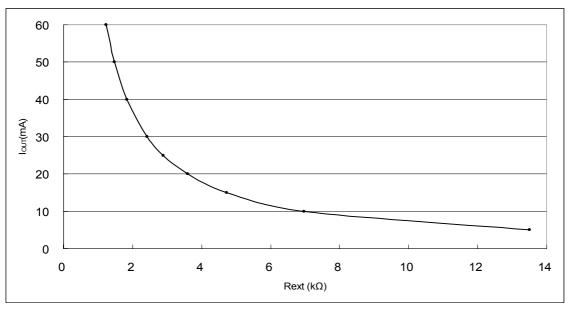


Figure 6

Also, the output current can be calculated from the equation:

$$V_{R-EXT} = 1.24V$$
;

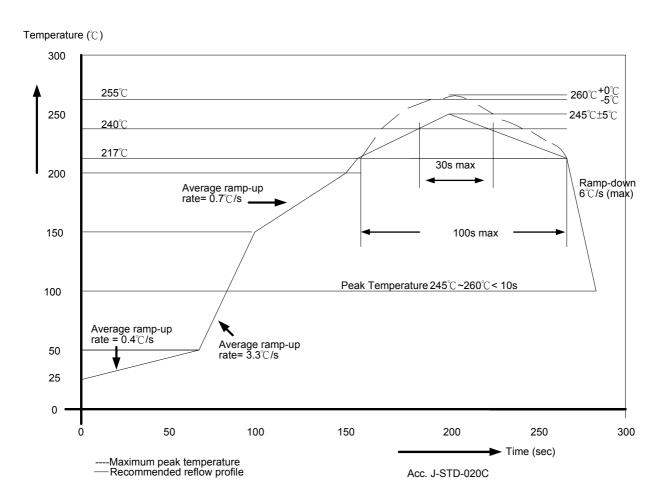
$$R_{\text{ext}} = (V_{\text{R-EXT}} / I_{\text{OUT}}) \times 59 = (1.24 \text{V} / I_{\text{OUT}}) \times 59,$$

$$I_{OUT}$$
 = ( $V_{R-EXT}/R_{ex}$ ) × 59 = (1.24V /  $R_{ext}$ ) × 59 within  $\pm$  6% chip skew

where  $R_{ext}$  is the resistance of the external resistor connected to R-EXT terminal and  $V_{R-EXT}$  is the voltage of R-EXT terminal. The magnitude of current (as a function of  $R_{ext}$ ) is around 56mA at 1.2k $\Omega$  and 30.5mA at 2.4k $\Omega$ .

## Soldering Process of "Pb-free & Green" Package Plating\*

Macroblock has defined "Pb-Free & Green" to mean semiconductor products that are compatible with the current RoHS requirements and selected **100% pure tin** (Sn) to provide forward and backward compatibility with both the current industry-standard SnPb-based soldering processes and higher-temperature Pb-free processes. Pure tin is widely accepted by customers and suppliers of electronic devices in Europe, Asia and the US as the lead-free surface finish of choice to replace tin-lead. Also, it is backward compatible to standard 215°C to 240°C reflow processes which adopt tin/lead (SnPb) solder paste. However, in the whole Pb-free soldering processes and materials, 100% pure tin (Sn) will all require up to 260°C for proper soldering on boards, referring to J-STD-020C as shown below.



\*Note: For details, please refer to Macroblock's "Policy on Pb-free & Green Package".

### Package Power Dissipation (PD)

The maximum power dissipation,  $P_D(max) = (T_{i,max} - T_a) / R_{th(i-a)}$ , decreases as the ambient temperature increases.

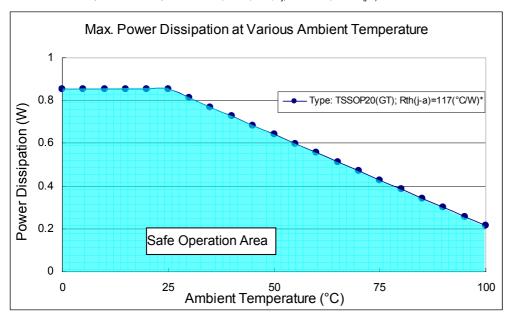


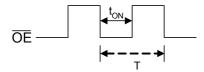
Figure 7

The maximum allowable package power dissipation is determined as  $P_D(max) = (T_{j,max} - T_a) / R_{th(j-a)}$ . When 16 output channels are turned on simultaneously, the actual package power dissipation is  $P_D(act) = (I_{DD} \ x \ V_{DD}) + (I_{OUT} \ x \ Duty \ x \ V_{DS} \ x \ 16)$ . Therefore, to keep  $P_D(act) \le P_D(max)$ , the allowable maximum output current as a function of duty cycle is:

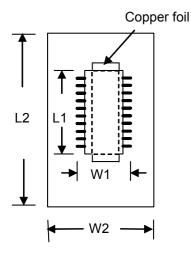
$$I_{OUT} = \{ [(T_j - T_a) / R_{th(j-a)}] - (I_{DD} \times V_{DD}) \} / V_{DS} / Duty / 16,$$
  
where  $T_i = 125^{\circ}C$ ;

Duty=  $t_{ON}$  / T;

ton: the time of LEDs turning on; T: OE signal period



\*Note: The empirical thermal resistor  $R_{th(j-a)}$  =117 °C/W; it is based on the following structure.



The PCB area L2xW2 is 4 times of the IC's area L1xW1.

The thickness of the PCB is 1.6 mm, copper foil 1 Oz. The thermal pad on the IC's bottom has to be mounted on the copper foil.

#### **TP Function (Thermal Protection)**

When the junction temperature exceeds the threshold,  $T_X$  (165°C), TP function turns off the output current. As soon as the temperature is below 165°C, the output current will be turned on again. The on-state and off-state switch at a high frequency; thus, the blinking is imperceptible. However, the average output current is limited, and therefore, the driver is protected from being overheated.

# Load Supply Voltage (V<sub>LED</sub>)

MBI1816 is designed to operate with adequate  $V_{DS}$  to achieve constant current.  $V_{DS}$  together with  $I_{OUT}$  should not exceed the package power dissipation limit,  $P_{D(max)}$ .

As in Figure 8,  $V_{DS} = V_{LED} - V_F$ , and  $V_{LED}$  is the load supply voltage.  $P_{D(act)}$  will be greater than  $P_{D(max)}$ , if  $V_{DS}$  drops too much voltage on the driver. In this case, it is recommended to use the lowest possible supply voltage or to set an external voltage reducer,  $V_{DROP}$ .

A voltage reducer lets  $V_{DS} = (V_{LED} - V_F) - V_{DROP}$ .

Resistors can be used in the applications as shown in Figure 8.

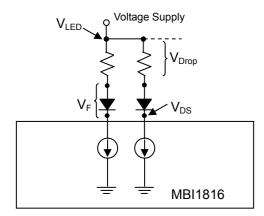
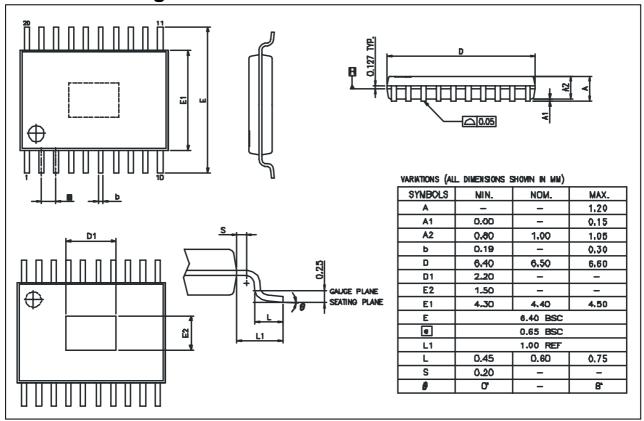


Figure 8

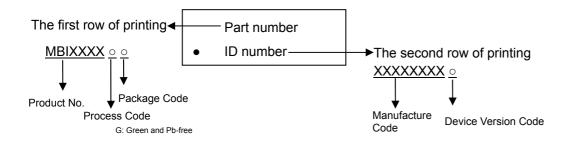
#### **Outline Drawing**



MBI1816GT Outline Drawing

Note: The unit for the outline drawing is mm.

## **Product Top-mark Information**



# **Product Revision History**

| Datasheet Version | Device Version Code |
|-------------------|---------------------|
| V1.00             | Not defined         |
| V1.01             | Α                   |
| V1.02             | В                   |
| V1.03             | В                   |

# **Product Ordering Information**

| Part Number | Package Type   | Weight (g) |
|-------------|----------------|------------|
| MBI1816GT   | TSSOP-173-0.65 | 0.0814     |

#### **Disclaimer**

Macroblock reserves the right to make changes, corrections, modifications, and improvements to their products and documents or discontinue any product or service without notice. Customers are advised to consult their sales representative for the latest product information before ordering. All products are sold subject to the terms and conditions supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

Macroblock's products are not designed to be used as components in device intended to support or sustain life or in military applications. Use of Macroblock's products in components intended for surgical implant into the body, or other applications in which failure of Macroblock's products could create a situation where personal death or injury may occur, is not authorized without the express written approval of the President of Macroblock. Macroblock will not be held liable for any damages or claims resulting from the use of its products in medical and military applications.

All text, images, logos and information contained on this document is the intellectual property of Macroblock.

Unauthorized reproduction, duplication, extraction, use or disclosure of the above mentioned intellectual property will be deemed as infringement.