

XJS78Lxx

3-TERMINALS POSITIVE VOLTAGE REGULATOR SERIES

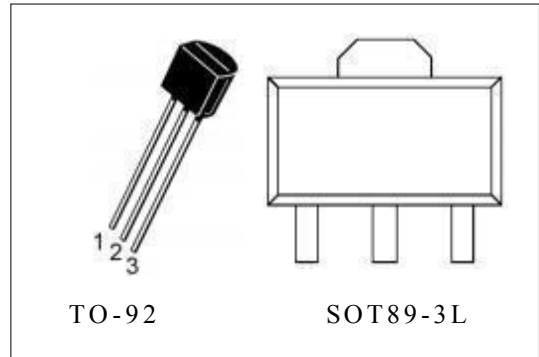
DESCRIPTION

The XJS78Lxx series of fixed voltage monolithic integrated circuit voltage regulators are suitable for applications that require supply up to 100 mA.

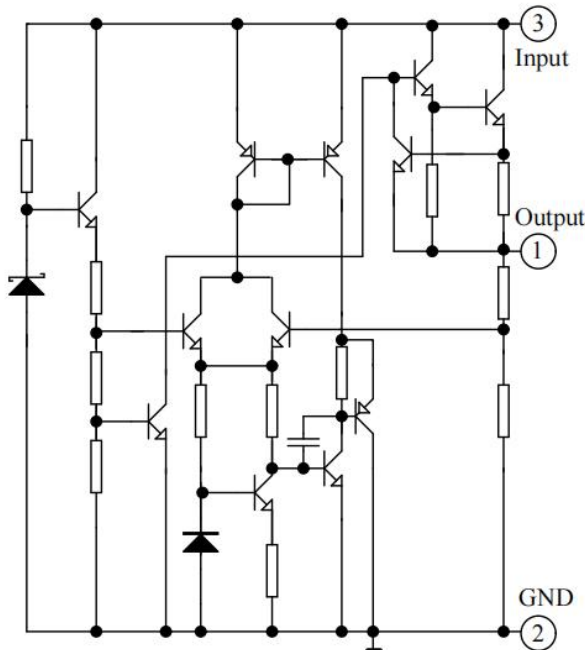
FEATURE

- Maximum output current of 100 mA
- Output voltage of 5 V, 6 V, 8 V, 9 V, 10 V, 12 V, 15 V and 24 V
- Thermal overload protection
- Short circuit current limiting

Outline Drawing



EQUIVALENT CIRCUIT



PIN CONNECTION

管脚定义	TO92	SOT89-3L
PIN1	Output	Output
PIN2	GND	GND
PIN3	Input	Input

ABSOLUTE MAXIMUM RATINGS

(Operating temperature range applies unless otherwise specified)

Characteristic	Symbol	Value	Unit
Input Voltage	V _i	V _o =5V~8V	25
		V _o =9V~15V	40
Operating Junction Temperature Range	T _{opr}	-20~120	°C
Storage Temperature Range	T _{stg}	-55~150	°C

XJS78L05 ELECTRICAL CHARACTERISTICS

(Unless otherwise specified: V_i=10V; I_o=40mA; C₁=0.33μF; C_o=0.1μF, 0<T_j<125°C)(Note 1)

Characteristics	Test conditions	Symbol	Min.	Typ.	Max.	Unit
Output Voltage	T _j =25°C	V _o	4.9	5.0	5.1	V
	7V≤V _i ≤20V; I _o =1mA~40mA		4.85		5.15	V
	7V≤V _i ≤V _{max} ; I _o =1mA~70mA		4.85		5.15	V (note 2)
Load Regulation	T _j =25°C; I _o =1mA~100mA	ΔV _o		11	60	mV
	T _j =25°C; I _o =1mA~40mA			5.0	30	mV
Line Regulation	T _j =25°C; 7V≤V _i ≤20V	ΔV _o		8	150	mV
	T _j =25°C; 8V≤V _i ≤20V			6	100	mV
Quiescent Current		I _q		2.0	5.5	mA
Quiescent Current Change	8V≤V _i ≤20V	ΔI _q			1.5	mA
	1mA≤I _o ≤40mA				0.1	mA
Output Noise Voltage	10Hz≤f≤100kHz	V _N		40		μV
Temperature Coefficient of V _o	I _o =5mA	ΔV _o /ΔT		-0.65		mV/°C
Ripple Rejection	10V≤V _i ≤20V; f=120Hz; T _j =25°C	RR	41	80		dB
Dropout Voltage	T _j =25°C	V _d		1.7		V

XJS78L06 ELECTRICAL CHARACTERISTICS

(Unless otherwise specified: $V_i=12V$; $I_o=40mA$; $C_1=0.33\mu F$; $C_o=0.1\mu F$; $0 < T_j < 125^\circ C$) (Note 1)

Characteristics	Test conditions	Symbol	Min.	Typ.	Max.	Unit
Output Voltage	$T_j=25^\circ C$	V_o	5.75	6.0	6.25	V
	$8.5V \leq V_i \leq 20V$; $I_o=1mA \sim 40mA$		5.7		6.3	V
	$8.5V \leq V_i \leq V_{max}$; $I_o=1mA \sim 70mA$		5.7		6.3	V (note 2)
Load Regulation	$T_j=25^\circ C$; $I_o=1mA \sim 100mA$	ΔV_o		12.8	80	mV
	$T_j=25^\circ C$; $I_o=1mA \sim 40mA$			5.8	40	mV
Line Regulation	$T_j=25^\circ C$; $8.5V \leq V_i \leq 20V$	ΔV_o		64	175	mV
	$T_j=25^\circ C$; $9V \leq V_i \leq 20V$			54	125	mV
Quiescent Current		I_q		3.9	6.0	mA
Quiescent Current Change	$9V \leq V_i \leq 20V$	ΔI_q			1.5	mA
	$1mA \leq I_o \leq 40mA$				0.1	mA
Output Noise Voltage	$10Hz \leq f \leq 100kHz$	V_N		49		μV
Temperature Coefficient of V_o	$I_o=5mA$	$\Delta V_o/\Delta T$		-0.75		mV/ $^\circ C$
Ripple Rejection	$10V \leq V_i \leq 20V$; $f=120Hz$; $T_j=25^\circ C$	RR	40	46		dB
Dropout Voltage	$T_j=25^\circ C$	V_d		1.7		V

XJS78L08 ELECTRICAL CHARACTERISTICS

(Unless otherwise specified: $V_i=14V$; $I_o=40mA$; $C_1=0.33\mu F$; $C_o=0.1\mu F$; $0 < T_j < 125^\circ C$) (Note 1)

Characteristics	Test conditions	Symbol	Min.	Typ.	Max.	Unit
Output Voltage	$T_j=25^\circ C$	V_o	7.7	8.0	8.3	V
	$10.5V \leq V_i \leq 23V$; $I_o=1mA \sim 40mA$		7.6		8.4	V
	$10.5V \leq V_i \leq V_{max}$; $I_o=1mA \sim 70mA$		7.6		8.4	V (note 2)
Load Regulation	$T_j=25^\circ C$; $I_o=1mA \sim 100mA$	ΔV_o		15	80	mV
	$T_j=25^\circ C$; $I_o=1mA \sim 40mA$			8.0	40	mV
Line Regulation	$T_j=25^\circ C$; $10.5V \leq V_i \leq 23V$	ΔV_o		10	175	mV
	$T_j=25^\circ C$; $11V \leq V_i \leq 23V$			8	125	mV
Quiescent Current		I_q		2.0	5.5	mA
Quiescent Current Change	$11V \leq V_i \leq 23V$	ΔI_q			1.5	mA
	$1mA \leq I_o \leq 40mA$				0.1	mA
Output Noise Voltage	$10Hz \leq f \leq 100kHz$	V_N		49		μV
Temperature Coefficient of V_o	$I_o=5mA$	$\Delta V_o/\Delta T$		-0.75		mV/ $^\circ C$
Ripple Rejection	$11V \leq V_i \leq 23V$; $f=120Hz$; $T_j=25^\circ C$	RR	39	70		dB
Dropout Voltage	$T_j=25^\circ C$	V_d		1.7		V

XJS78L09 ELECTRICAL CHARACTERISTICS

(Unless otherwise specified: $V_i=15V$; $I_o=40mA$; $C_1=0.33\mu F$; $C_o=0.1\mu F$, $0 < T_j < 125^\circ C$) (Note 1)

Characteristics	Test conditions	Symbol	Min.	Typ.	Max.	Unit
Output Voltage	$T_j=25^\circ C$	V_o	8.64	9.0	9.36	V
	$11.5V \leq V_i \leq 24V$; $I_o=1mA \sim 40mA$		8.55		9.45	V
	$11.5V \leq V_i \leq V_{max}$; $I_o=1mA \sim 70mA$		8.55		9.45	V (note 2)
Load Regulation	$T_j=25^\circ C$; $I_o=1mA \sim 100mA$	ΔV_o		20	90	mV
	$T_j=25^\circ C$; $I_o=1mA \sim 40mA$			10	45	mV
Line Regulation	$T_j=25^\circ C$; $11.5V \leq V_i \leq 24V$	ΔV_o		90	200	mV
	$T_j=25^\circ C$; $13V \leq V_i \leq 24V$			100	150	mV
Quiescent Current		I_q		2.0	6.0	mA
Quiescent Current Change	$13V \leq V_i \leq 24V$	ΔI_q			1.5	mA
	$1mA \leq I_o \leq 40mA$				0.1	mA
Output Noise Voltage	$10Hz \leq f \leq 100kHz$	V_N		49		μV
Temperature Coefficient of V_o	$I_o=5mA$	$\Delta V_o/\Delta T$		-0.75		mV/ $^\circ C$
Ripple Rejection	$12V \leq V_i \leq 23V$; $f=120Hz$; $T_j=25^\circ C$	RR	38	44		dB
Dropout Voltage	$T_j=25^\circ C$	V_d		1.7		V

XJS78L10 ELECTRICAL CHARACTERISTICS

(Unless otherwise specified: $V_i=16V$; $I_o=40mA$; $C_1=0.33\mu F$; $C_o=0.1\mu F$, $0 < T_j < 125^\circ C$) (Note 1)

Characteristics	Test conditions	Symbol	Min.	Typ.	Max.	Unit
Output Voltage	$T_j=25^\circ C$	V_o	9.6	10	10.4	V
	$12.5V \leq V_i \leq 23V$; $I_o=1mA \sim 40mA$		9.5		10.5	V
	$12.5V \leq V_i \leq V_{max}$; $I_o=1mA \sim 70mA$		9.5		10.5	V (note 2)
Load Regulation	$T_j=25^\circ C$; $I_o=1mA \sim 100mA$	ΔV_o		20	94	mV
	$T_j=25^\circ C$; $I_o=1mA \sim 40mA$			10	47	mV
Line Regulation	$T_j=25^\circ C$; $12.5V \leq V_i \leq 23V$	ΔV_o		100	220	mV
	$T_j=25^\circ C$; $14V \leq V_i \leq 23V$			200	170	mV
Quiescent Current		I_q		4.2	6.5	mA
Quiescent Current Change	$12.5V \leq V_i \leq 23V$	ΔI_q			1.5	mA
	$1mA \leq I_o \leq 40mA$				0.1	mA
Output Noise Voltage	$10Hz \leq f \leq 100kHz$	V_N		74		μV
Temperature Coefficient of V_o	$I_o=5mA$	$\Delta V_o/\Delta T$		-0.95		mV/ $^\circ C$
Ripple Rejection	$15V \leq V_i \leq 23V$; $f=120Hz$; $T_j=25^\circ C$	RR	38	43		dB
Dropout Voltage	$T_j=25^\circ C$	V_d		1.7		V

XJS78L12 ELECTRICAL CHARACTERISTICS

(Unless otherwise specified: $V_i=19V$; $I_o=40mA$; $C_1=0.33\mu F$; $C_o=0.1\mu F$, $0 < T_j < 125^\circ C$) (Note 1)

Characteristics	Test conditions	Symbol	Min.	Typ.	Max	Unit
Output Voltage	$T_j=25^\circ C$	V_o	11.5	15	15.6	V
	$14.5V \leq V_i \leq 27V$; $I_o=1mA \sim 40mA$		11.4		12.6	V
	$14.5V \leq V_i \leq V_{max}$; $I_o=1mA \sim 70mA$		11.4		12.6	V (note 2)
Load Regulation	$T_j=25^\circ C$; $I_o=1mA \sim 100mA$	ΔV_o		25	150	mV
	$T_j=25^\circ C$; $I_o=1mA \sim 40mA$			12	75	mV
Line Regulation	$T_j=25^\circ C$; $14.5V \leq V_i \leq 27V$	ΔV_o		25	300	mV
	$T_j=25^\circ C$; $16V \leq V_i \leq 27V$			20	250	mV
Quiescent Current		I_q		2.0	6.0	mA
Quiescent Current Change	$16V \leq V_i \leq 27V$	ΔI_q			1.5	mA
	$1mA \leq I_o \leq 40mA$				0.1	mA
Output Noise Voltage	$10Hz \leq f \leq 100kHz$	V_N		80		μV
Temperature Coefficient of V_o	$I_o=5mA$	$\Delta V_o / \Delta T$		-1.0		mV/°C
Ripple Rejection	$1.5V \leq V_i \leq 2.5V$; $f=120Hz$; $T_j=25^\circ C$	RR	37	65		dB
Dropout Voltage	$T_j=25^\circ C$	V_d		1.7		V

XJS78L15 ELECTRICAL CHARACTERISTICS

(Unless otherwise specified: $V_i=23V$; $I_o=40mA$; $C_1=0.33\mu F$; $C_o=0.1\mu F$, $0 < T_j < 125^\circ C$) (Note 1)

Characteristics	Test conditions	Symbol	Min.	Typ.	Max	Unit
Output Voltage	$T_j=25^\circ C$	V_o	14.4	15	15.6	V
	$17.5V \leq V_i \leq 30V$; $I_o=1mA \sim 40mA$		14.25		15.75	V
	$17.5V \leq V_i \leq V_{max}$; $I_o=1mA \sim 70mA$		14.25		15.75	V (note 2)
Load Regulation	$T_j=25^\circ C$; $I_o=1mA \sim 100mA$	ΔV_o		20	150	mV
	$T_j=25^\circ C$; $I_o=1mA \sim 40mA$			25	150	mV
Line Regulation	$T_j=25^\circ C$; $17.5V \leq V_i \leq 30V$	ΔV_o		25	150	mV
	$T_j=25^\circ C$; $20V \leq V_i \leq 30V$			15	75	mV
Quiescent Current		I_q		2.2	6.5	mA
Quiescent Current Change	$20V \leq V_i \leq 30V$	ΔI_q			1.5	mA
	$1mA \leq I_o \leq 40mA$				0.1	mA
Output Noise Voltage	$10Hz \leq f \leq 100kHz$	V_N		90		μV
Temperature Coefficient of V_o	$I_o=5mA$	$\Delta V_o / \Delta T$		-1.3		mV/°C
Ripple Rejection	$18.5V \leq V_i \leq 28.5V$; $f=120Hz$; $T_j=25^\circ C$	RR	34	63		dB
Dropout Voltage	$T_j=25^\circ C$	V_d		1.7		V

XJS78L18 ELECTRICAL CHARACTERISTICS

(Unless otherwise specified: $V_i=27V$; $I_o=40mA$; $C_1=0.33\mu F$; $C_o=0.1\mu F$, $0 < T_j < 125^\circ C$)(Note 1)

Characteristics	Test conditions	Symbol	Min.	Typ.	Max.	Unit
Output Voltage	$T_j=25^\circ C$	V_o	17.3	18	18.7	V
	$21V \leq V_i \leq 33V$; $I_o=1mA \sim 40mA$		17.1		18.9	V
	$21V \leq V_i \leq V_{max}$; $I_o=1mA \sim 70mA$		17.1		18.9	V (note 2)
Load Regulation	$T_j=25^\circ C$; $I_o=1mA \sim 100mA$	ΔV_o		30	170	mV
	$T_j=25^\circ C$; $I_o=1mA \sim 40mA$			15	85	mV
Line Regulation	$T_j=25^\circ C$; $21V \leq V_i \leq 33V$	ΔV_o		145	300	mV
	$T_j=25^\circ C$; $22V \leq V_i \leq 33V$			135	250	mV
Quiescent Current		I_q		2.0	6.0	mA
Quiescent Current Change	$21V \leq V_i \leq 33V$	ΔI_q			1.5	mA
	$1mA \leq I_o \leq 40mA$				0.1	mA
Output Noise Voltage	$10Hz \leq f \leq 100kHz$	V_N		150		μV
Temperature Coefficient of V_o	$I_o=5mA$	$\Delta V_o/\Delta T$		-1.8		mV/ $^\circ C$
Ripple Rejection	$23V \leq V_i \leq 33V$; $f=120Hz$; $T_j=25^\circ C$	RR	34	48		dB
Dropout Voltage	$T_j=25^\circ C$	V_d		1.7		V

XJS78L24 ELECTRICAL CHARACTERISTICS

(Unless otherwise specified: $V_i=33V$; $I_o=40mA$; $C_1=0.33\mu F$; $C_o=0.1\mu F$, $0 < T_j < 125^\circ C$)(Note 1)

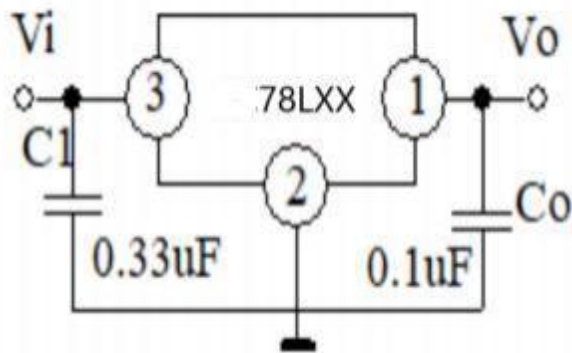
Characteristics	Test conditions	Symbol	Min.	Typ.	Max.	Unit
Output Voltage	$T_j=25^\circ C$	V_o	23	24	25	V
	$27V \leq V_i \leq 38V$; $I_o=1mA \sim 40mA$		22.8		25.2	V
	$27V \leq V_i \leq V_{max}$; $I_o=1mA \sim 70mA$		22.8		25.2	V (note 2)
Load Regulation	$T_j=25^\circ C$; $I_o=1mA \sim 100mA$	ΔV_o		40	200	mV
	$T_j=25^\circ C$; $I_o=1mA \sim 40mA$			20	100	mV
Line Regulation	$T_j=25^\circ C$; $27V \leq V_i \leq 38V$	ΔV_o		160	300	mV
	$T_j=25^\circ C$; $28V \leq V_i \leq 38V$			150	250	mV
Quiescent Current		I_q		2.2	6.0	mA
Quiescent Current Change	$27V \leq V_i \leq 38V$	ΔI_q			1.5	mA
	$1mA \leq I_o \leq 40mA$				0.1	mA
Output Noise Voltage	$10Hz \leq f \leq 100kHz$	V_N		200		μV
Temperature Coefficient of V_o	$I_o=5mA$	$\Delta V_o/\Delta T$		-2.0		mV/ $^\circ C$
Ripple Rejection	$27V \leq V_i \leq 38V$; $f=120Hz$; $T_j=25^\circ C$	RR	34	45		dB
Dropout Voltage	$T_j=25^\circ C$	V_d		1.7		V

XJS78Lxx

Note 1: The Maximum steady state usable output current and input voltage are very dependent on the heating sinking and/or lead temperature length of the package. The data above represent pulsed test conditions with junction temperatures as indicated at the initiation of test.

Note 2: Power dissipation $< 0.75\text{ W}$

TYPICAL APPLICATION CIRCUIT



Note 1: To specify an output voltage, substitute voltage value for "xx".

Note 2: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.